

NDnano Summer Undergraduate Research 2017 Project Summary

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Project title: Generation of polymeric shell encapsulating microdroplets by AC Electro spray and fabrication of Silicon Nanopore array by inverted pyramid technique

New skills acquired during summer research:

I have learnt the basics of generating microdroplets using alternating current electro spray. I have also acquired various Nano-level fabrication techniques. I have undergone gowning training for the clean room. I have learnt to use machines like Evaporator, Plasma Enhanced Vapor Chemical Deposition (PEVCD), Reactive Ion Etching (RIE), Autostep and Stepper for mask making.

Abstract:

We present the method to produce microdroplets encapsulated by styrene shell by AC Electro spray method. We used the noble idea of Ohmic heating generated due to applied electric field at the tip of capillary to polymerize the styrene thus forming the shell. We have also fabricated Silicon nanopore array by inverted pyramid technique to achieve multi-target quantification of droplets.

Practical application/end use of research:

The encapsulation of liquid droplet can protect the droplet from outside deteriorating effects of environment such as oxidation and moisture particularly in pharmaceutical industry. If composition of polymer and parameters of AC spray is selected appropriately then we can make more stable droplets of serum containing DNA for PCR and then using these droplets for analysis. The benefits of nanopore array is that we can try AC spray out of the entire array but with different primers in each nanopore, thus resulting in massively large multi-target quantification.

References:

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The AC spray chip setup is shown in figure 1. The styrene solution's flow is controlled by syringe pump and water solution's flow is controlled by air pump. The 10g of styrene solution's composition includes 5g of styrene, 2.088g of DVB and 2.852 g of Span-80. The water solution has 10mM KCl dissolved in it.

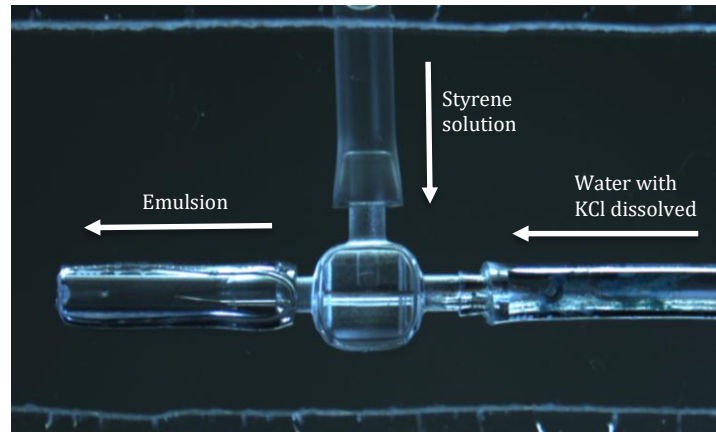


Figure 1: Typical chip setup for AC electro spray

The electric field generated by wave generator is applied to water solution. There is Ohmic heating at the tip of capillary due to applied electric field. As water is forced through the capillary, it forms droplet at tip and styrene around the droplet get polymerized because of heat thus forming shell around droplet.

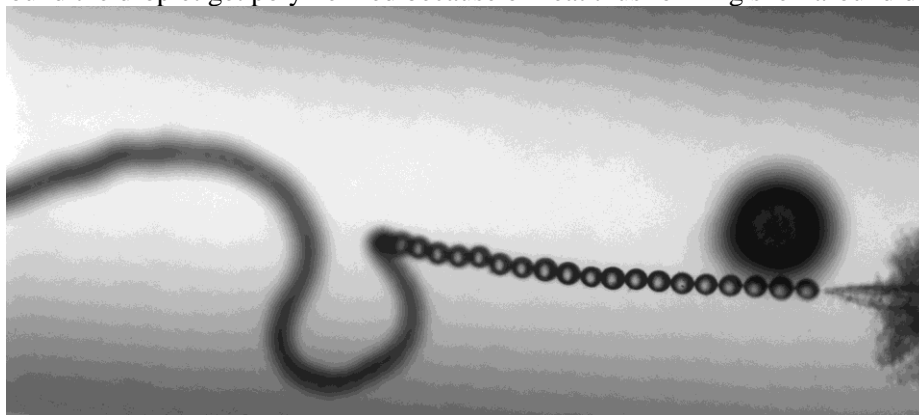


Figure 2: The formation of thread of mono-disperse droplets with styrene shell

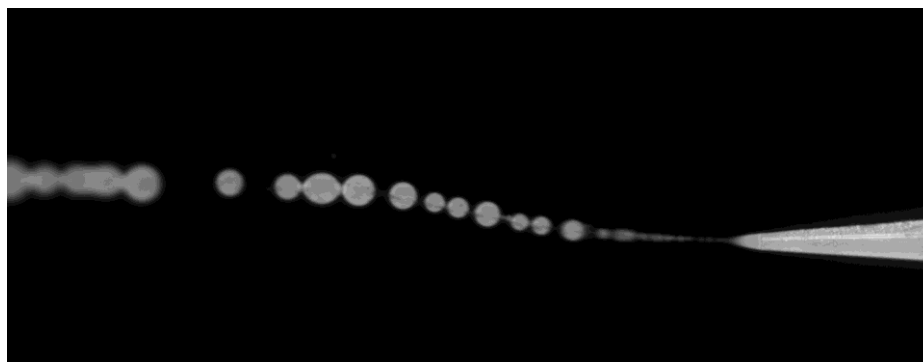


Figure 3: Droplet formation under fluorescence microscopy. The droplets contain water mixed with fluorescence die.

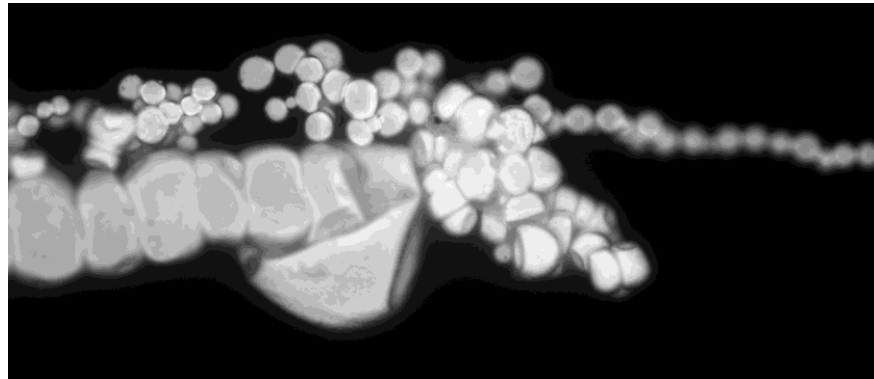
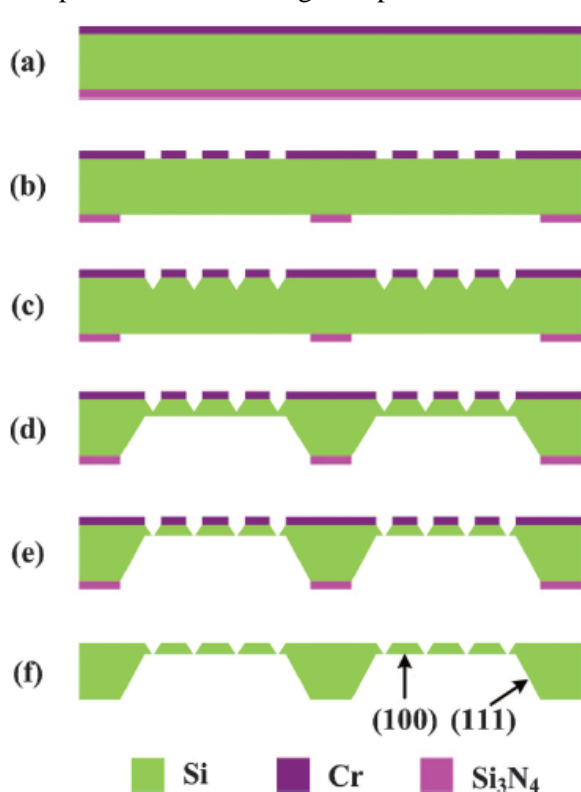


Figure 4: The droplets with distorted shape shows that there is shell covering them and it has squeezed because of liquid extraction from it

The second part of the project includes the fabrication of Silicon Nanopore Array. Silicon wafer used were 500 μm thick, double-sided polished, <100> oriented wafers with diameters of 100 mm. The mask designing was done by software called L-edit. The two masks were designed and used. One with three smaller windows of size 4 μm X 4 μm , 5 μm X 5 μm and 6 μm X 6 μm and other with three 1200 μm X 1200 μm size window. These masks of size 5'' are fabricated by machine called Stepper.

The process of fabricating nanopore is described as follows



(a) 700nm thick Si_3N_4 is deposited first using PECVD technique on Si and then 300nm thick Cr layer is deposited using evaporator machine.

(b) The photoresist is deposited on both side using spin and bake technique. Mask with smaller window is used to transfer the pattern to Cr layer using Autostepper machine and mask with larger window is used to transfer the large window pattern to Si_3N_4 layer.

(c) Grooves with slanted edges were made using KOH wet etching method at 50 $^\circ\text{C}$ on Cr layer first.

(d) KOH wet etching at 70 $^\circ\text{C}$ on Si_3N_4 side is done to thin the substrate at faster rate.

(e) KOH wet etching is now performed at 30 $^\circ\text{C}$ to precisely control the opening of the pores.

(f) Layers of Cr and Si_3N_4 are removed.

Figure 5: Steps for fabricating inverted Si nanopore [5,6]